Gianluca Giustolisi

Curriculum Vitæ

Short bio

Gianluca Giustolisi was born in Catania, Italy, in 1971.
He received the Laurea degree (summa cum laude) in electronic engineering and the Ph.D. degree in electrical engineering from the University of Catania in 1995 and 1999, respectively.
Currently he is **Associate Professor** of electronics at the "Dipartimento di Ingegneria Elettrica Elettronica e Informatica" (DIEEI) of the University of Catania.
In 2002 he joined the DIEEI of the University of Catania as Assistant Professor where he became Associate Professor in 2005. In 2017 he obtained the **National Scientific Qualification** for *full professor* in Electronics.
Since 2003 he has been teaching courses on electron devices and on analog electronics in both the BSc and the MSc in electronic engineering. He was also teacher of a course in electronic laboratory at the MSc in electronic engineering, and of a course in Biomedical technologies at the Specialization School in Digestive Surgery.
His main research interests reflect his teaching activity in analog circuits with particular emphasis on feedback circuits, compensation techniques, voltage regulators, bandgap voltage references, low-voltage circuits and device modeling.
Some of his research activity has been developed in cooperation with University of Rome "Tor Vergata", with CNR-IMM (Consiglio Nazionale delle Ricerche — Institute for Microelectronics and Microsystems) and with INFN-LNS (National Institute for Nuclear Physics — Laboratori Nazionali del Sud).
He is author of more than 95 scientific papers in referred international journals and conferences. He is author of the italian course-book "Introduzione ai Dispositivi Elettronici", published by Franco Angeli.
From 2008 to 2017 he served as Associate Editor of the "Journal of Circuits, Systems, and Computers". Since 2016 he has been member of the Editorial Board of the "Microelectronics Journal – Elsevier".
He has been *IEEE member* since 1999 and became *Senior member* in 2017.

Education

1995–1999 **Ph.D. degree in Electrical Engineering**, *University of Catania*, Catania, Italy.
Ph.D. Thesis: Progettazione di un controllore Fuzzy in tecnologia CMOS con approccio a condensatore commutato.

1989–1995 **Laurea degree in Electronic Engineering**, *University of Catania*, Catania, Italy.
Thesis: Circuito integrato in tecnologia CMOS per la gestione centralizzata di un sistema di sensori.

Experience

2005–present **Associate Professor of Electronics**, *University of Catania*, Catania, Italy.

2002–2005 **Assistant Professor of Electronics**, *University of Catania*, Catania, Italy.

1999–2002 **Research grant**, *University of Catania*, Catania, Italy.
Research Topic: Low-voltage and low-power circuits and systems.

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Funded projects

2005 **PRIN 2005**, *National research project*, Associated Investigator for the Catania Unit, (2yrs).
A/D converters for integrated control systems with high-rejection to electromagnetic interferences.

2004 **UNICT 2004**, *Local research project*, Principal investigator, (1yrs).
Stadi di uscita CMOS a bassa tensione di alimentazione.

2003 **UNICT 2003**, *Local research project*, Principal investigator, (1yrs).
Convertitori A/D integrati per elaborazione fuzzy.

2000 **Progetto Giovani Ricercatori**, *Local research project*, Principal investigator, (2yrs).
Tecniche a basso consumo e bassa tensione di alimentazione negli stadi di uscita e di ingresso per amplificatori operazionali.

Teaching Activity

2019–present	Elettronica	*B.Sc. Informatic Engineering*
2020–present	Analog Electronics	*M.Sc. Electronic Engineering*
2010–2020	Dispositivi Elettronici	*M.Sc. Electronic Engineering*
2012–2019	Electronics II	*B.Sc. Electronic Engineering*
2002–2011	Dispositivi Elettronici	*B.Sc. Electronic Engineering*
2004–2010	Laboratorio di Microelettronica	*M.Sc. Electronic Engineering*
2002–2009	Tecnologie Biomediche	*Specialization School in Digestive Surgery*
2002–2004	Elettronica Analogica II	*B.Sc. Automation Engineering*

Service Activity

2018–present **Component of the Quality Assurance Presidium**, *University of Catania*.

2018–present **Representative of Scientific Unit**, *Unit of Catania for the* Società Italiana di Elettronica *(SIE)*.

2017–present **Evaluation Expert**, *panel of the System Experts for the* National Agency of the Evaluation of Universities and Research institutes *(ANVUR)*.
- Periodic accreditation Università degli Studi di Bari "Aldo Moro" (12th–16th November 2018)
- Periodic accreditation Libera Università di Bolzano (8th–12th April 2019)

2016–present **Member of the Editorial Board**, *Microelectronics Journal – Elsevier*.

2014–present **Scientific Expert**, *panel of the Experts in Basic Research of the Ministero dell'Istruzione, dell'Università e della Ricerca (MIUR)*.
- Project evaluation for the National Project "Futuro in Ricerca 2013"
- Proposal evaluation for a Research Grant (Università della Calabria, Assegni di Ricerca 2014)
- Project evaluation for the National Project "PRIN 2015"

2013–present **Evaluation Expert**, *panel of the Disciplinary Experts of the* National Agency for the Evaluation of Universities and Research institutes *(ANVUR)*.
- Periodic accreditation Università Campus Bio-Medico di Roma (5th–9th October 2015)
- Periodic accreditation Politecnico di Torino (23rd–27th May 2016)
- Initial accreditation 2017
- Periodic accreditation Università degli Studi di Cagliari (16th–20th October 2017)
- Periodic accreditation Università degli Studi di Napoli "Federico II" (11th–15th December 2017)
- Initial accreditation 2018
- Periodic accreditation Politecnico di Milano (11th–15th November 2019)

2019 **Technical Program Chair**, *15th Conference on Ph.D. Research in Microelectronics and Electronics*.
PRIME 2019 15th–18th of July 2019, Lausanne, Switzerland

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2018 **Guest Editor**, *Integration, the VLSI Journal*, Special Issue on PRIME and SMACD 2018, Elsevier.

2018 **Technical Program Chair**, *14th Conference on Ph.D. Research in Microelectronics and Electronics*.
PRIME 2018 2nd–5th of July 2018, Prague, Czech Republic

2017 **Guest Editor**, *Integration, the VLSI Journal*, Special Issue on PRIME and SMACD 2017, Elsevier.

2017 **Technical Program Chair**, *23rd European Conference on Circuit Theory and Design*.
ECCTD 2017, 4th–6th of September 2017, Catania CT, Italy

2017 **Technical Program Chair**, *13th Conference on Ph.D. Research in Microelectronics and Electronics*.
PRIME 2017, 12th–15th of June 2017, Giardini-Naxos ME, Italy

2008–2017 **Associate editor**, *Journal of Circuits, Systems, and Computers*.

2012–2016 **President of the degree course**, *M.Sc. in Electronic Engineering*.

2009–2012 **President of the didactic area in electronics**, *B.Sc. and M.Sc. in Electronic Engineering, Automation Engineering and Telecommunication Engineering*.

Languages

Italian Native language

English C1

Publications

Articles

[1] G. Giustolisi, G. Palmisano, and G. Palumbo, "1.5V power supply CMOS voltage squarer," *IEE Electronics Letters*, vol. 33, no. 3, pp. 1134–1136, Jun. 1997.

[2] G. Giustolisi, G. Palmisano, and G. Palumbo, "Switched capacitor compatible minimum-maximum function," *IEE Electronics Letters*, vol. 36, no. 1, pp. 35–36, Jan. 2000.

[3] G. Giustolisi, G. Palmisano, and G. Palumbo, "CMRR Frequency Response of CMOS Operational Transconductance Amplifiers," *IEEE Transactions on Instrumentation and Measurement*, vol. 49, no. 1, pp. 137–143, feb 2000.

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Conferences

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